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## NOTICE OF ALLOWANCE AND FEE(S) DUE

56639 7590 EMPK & Shiloh, LLP

c/o Landon IP, Inc.

Suite 450 Alexandria, VA 22314 08/24/2009

EXAMINER

NGUYEN, DANG T

PAPER NUMBER

ART UNIT

DATE MAILED: 08/24/2009

10/537 857	06/07/2005	Assaf Shappin		7211	
APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	

TITLE OF INVENTION: METHOD, CIRCUIT AND SYSTEM FOR ERASING ONE OR MORE NON-VOLATILE MEMORY CELLS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1510	\$300	\$0	\$1810	11/24/2009

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

#### HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

A. If the status is the same, pay the TOTAL FEE(S) DUE shown above.

B. If the status above is to be removed, check box 5b on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above, or

If the SMALL ENTITY is shown as NO:

A. Pay TOTAL FEE(S) DUE shown above, or

B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FFE: shown above.

II. PART B - FEE(S) TRANSMITTAL, or its equivalent, must be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted. If an equivalent of Part B is filed, a request to reapply a previously paid issue fee must be clearly made, and delays in processing may occur due to the difficulty in recognizing the paper as an equivalent of Part B.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

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Suite 450 Alexandria, VA	22314						(Depositor's name)
			<u> </u>				(Signature)
							(Date)
APPLICATION NO.	FILING DATE		FIRST NAMED INVENTOR		ATTOE	RNEY DOCKET NO.	CONFIRMATION NO.
10/537,857 TITLE OF INVENTION	06/07/2005 G: METHOD, CIRCUIT	AND SYSTEM FOR ER	Assaf Shappir ASING ONE OR MORE N	ON-VOLATILE M	IEMO!	RY CELLS	7211
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nonprovisional	NO	\$1510	\$300	\$0	\$0 \$1810		11/24/2009
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NGUYEN	, DANG T	2824	365-185290	•			
"Fee Address" ind PTO/SB/47; Rev 03-0 Number is required.  3. ASSIGNEE NAME A	ondence address (or Cha 3/122) attached. ication (or "Fee Address 12 or more recent) attach ND RESIDENCE DAT2 less an assignee is ident h in 37 CFR 3.II. Comp	nge of Correspondence "Indication form ed. Use of a Customer A TO BE PRINTED ON	2. For printing on the p (I) the names of up to or agents OR, alternativ (2) the name of a singl registered attorney or a 2 registered patent atto listed, no name will be THE PATENT (print or tyr data will appear on the p T a substitute for filing an (B) RESIDENCE: CETY	3 registered patent vely, e firm (having as a regent) and the names meys or agents. If no printed.	attorn membe s of up o name	er a 2e to be to 3entified below, the d	ocument has been filed for
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#### UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS

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APPLICATION N	O. F	TLING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/537,857		06/07/2005	Assaf Shappir	7211		
56639	7590	08/24/2009		EXAM	IINER	
EMPK & SI	EMPK & Shiloh, LLP			NGUYEN, DANG T		
c/o Landon II				ART UNIT	PAPER NUMBER	
1700 Diagonal Road Suite 450				2824 DATE MAILED: 08/24/200	19	

# Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 0 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 0 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

# Application No. Applicant(s) 10/537.857 SHAPPIR ET AL. Notice of Allowability Examiner Art Unit DANG T NGUYEN 2824 -- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308. This communication is responsive to 14 May 2009. The allowed claim(s) is/are 1,4-10 and 12-19. 3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) $\square$ All b) ☐ Some\* c) ☐ None of the: 1. T Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)). \* Certified copies not received: \_\_\_\_\_. Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. 4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient. CORRECTED DRAWINGS (as "replacement sheets") must be submitted. (a) Including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d). 6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL. Attachment(s) 1. Notice of References Cited (PTO-892) 5. Notice of Informal Patent Application 2. Notice of Draftperson's Patent Drawing Review (PTO-948) Interview Summary (PTO-413), Paper No./Mail Date 3. Information Disclosure Statements (PTO/SB/08), 7. X Examiner's Amendment/Comment Paper No./Mail Date 4. ☐ Examiner's Comment Regarding Requirement for Deposit 8. X Examiner's Statement of Reasons for Allowance of Biological Material Other .

/Dang T Nguyen/ Primary Examiner, Art Unit 2824 Application/Control Number: 10/537,857 Page 2

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### DETAILED ACTION

This office action is in response to applicant's arguments received on 05/14/09.

2. Claims 1, 9 and 19 have been amended. Claims 1 – 19 are pending in this case.

Claims 1, 9 and 19 are independent claims.

# Examiner's Amendment

3. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Lior Meir on Aug. 14, 2009.

The application has been amended as follows:

- ("NVM") cells comprising: applying to the one or more NVM cells an erase pulse having a predominantly non-flat and non-linear voltage profile, and wherein said erase pulse has a predefined voltage profile selected from the group consisting of ramp-like, exponential-growth-like, asymptote-like and stepped.
- 9. (currently amended) A circuit for erasing one or more non-volatile memory ("NVM") cells comprising; an erase pulse source to produce an erase pulse having a predominantly non-flat and non-linear voltage profile, and wherein said erase pulse source is adapted to produce an erase pulse having a predefined voltage profile selected from the group consisting of ramp-like, exponential-growth-like, asymptote-like and stepped.

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19. (currently amended) A system for erasing one or more non-volatile memory ("NVM") cells comprising: A NVM array, and an erase pulse source to produce an erase pulse having a predominantly non-flat and non-linear voltage profile, and wherein said erase pulse source is adapted to produce an erase pulse having a predefined voltage profile selected from the group consisting of ramp-like, exponential-growth-like, asymptote-like and stepped.

Claim 4, line 30 changes "claim 3" To - -claim 1- - Cancel Claims 2, 3, 10 and 11.

# Allowable Subject Matter

- Claims 1, 4 9 and 12 19 are allowed.
- 5. The following is an examiner's statement of reasons for allowance:

With respect to claim 1, the prior art of record fails to anticipate or render obvious a method of erasing one or more non-volatile memory ("NVM") cells, in combination with other limitations, comprising: "wherein said erase pulse has a predefined voltage profile selected from the group consisting of ramp-like, exponential-growth-like, asymptote-like and stepped."

With respect to claim 9, the prior art of record fails to anticipate or render obvious a circuit for erasing one or more non-volatile memory ("NVM") cells, in combination with other limitations, comprising: "wherein said erase pulse source is

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adapted to produce an erase pulse having a predefined voltage profile selected from the group consisting of ramp-like, exponential-growth-like, asymptote-like and stepped."

With respect to claim 19, the prior art of record fails to anticipate or render obvious a system for erasing one or more non-volatile memory ("NVM") cells, in combination with other limitations, comprising: "wherein said erase pulse source is adapted to produce an erase pulse having a predefined voltage profile selected from the group consisting of ramp-like, exponential-growth-like, asymptote-like and stepped."

Dependent claims 4-8 and 12-18 are allowed based on the allowance of the respective independent claims 1 and 9 above.

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Nagaoka (Patent No.: US 6,512,501 B1) discloses applying to the one or more NVM cells an erase pulse having a predominantly non-flat and non-linear voltage profile but it does not disclose the erase pulse has a predefined voltage profile selected from the group consisting of ramp-like, exponential-growth-like, asymptote-like and stepped.

7. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

### Prior art

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 The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Nagaoka Patent No. US 6,512,501 B1 Date of Patent: Jan. 28, 2003

# Contact Information

 Any inquiry concerning this communication from the examiner should be directed to Dang Nguyen, who can be reached by telephone at (571) 272-1955. Normal contact times are M-F, 8:00 AM - 4:30 PM.

Upon an unsuccessful attempt to contact the examiner, the examiner's supervisor, Richard Elms, may be reached at (571) 272-1869.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist, whose telephone number is (703) 305-3900. The faxed phone number for organization where this application or proceeding is assigned is (703) 872-9306.

Information regarding the Status of an application may be obtained from the patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free) or EBC@uspto.gov.

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8/14/2009 /Dang T Nguyen/

Primary Examiner, Art Unit 2824